Abstracts

A 94 GHz MMIC Tripler Using Anti-Parallel Diode Arrays for Idler Separation

M. Cohn, R.G. Freitag, H.G. Henry, J.E. Degenford and D.A. Blackwell. "A 94 GHz MMIC Tripler Using Anti-Parallel Diode Arrays for Idler Separation." 1994 MTT-S International Microwave Symposium Digest 94.2 (1994 Vol. II [MWSYM]): 763-766.

A 31.33 to 94 GHz millimeter wave frequency tripler is described which employs large numbers of high cutoff frequency Schottky barrier diodes in compact series/parallel arrays. The high breakdown voltage, distributed nature of the diode array and all of the diodes being thermally in parallel result in high power handling capability.

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